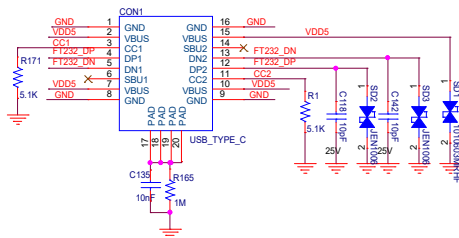
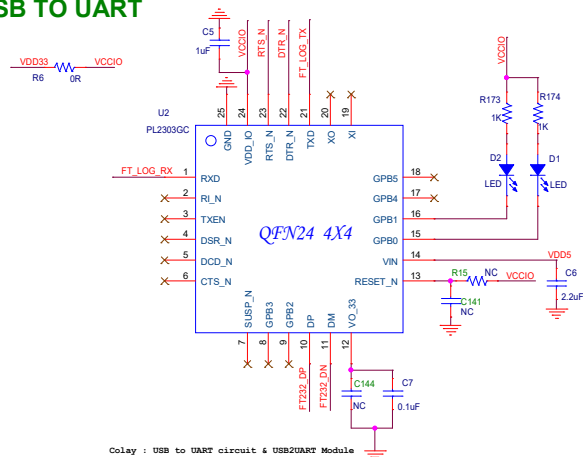


USB&POWER

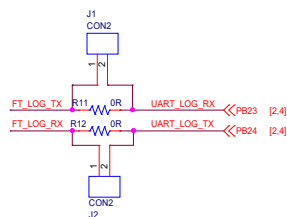
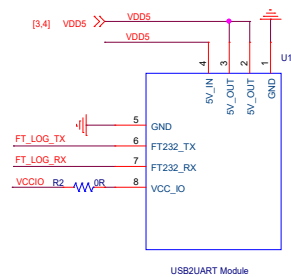
USB TypeC (Power and FT232)



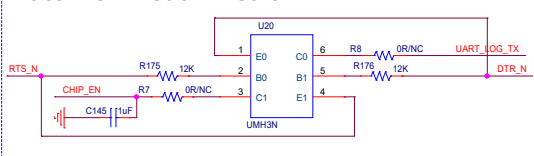
PL2303GC QFN24 4X4 USB TO UART



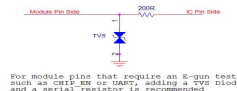
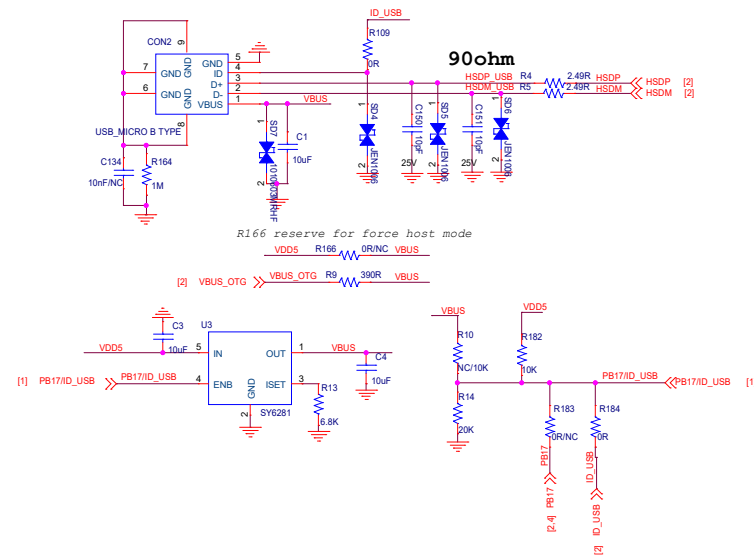
Colay : USB to UART circuit & USB2UART Module



Auto-Download Circuit

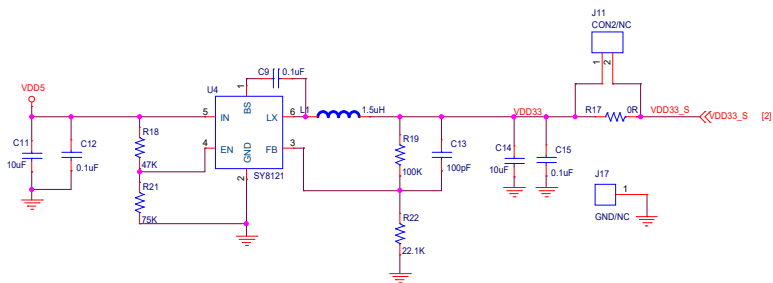


USB OTG



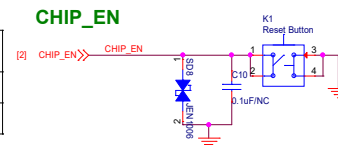
For module plus that requires an 8-pin test, such as CHIP_EN or UART, adding a 2V5 diode and a serial Resistor is recommended

5V -> +3.3V SWR



CHIP_ENABLE CONTROL	
SW CTRL Func.	
Press	pull low
Release	default setting

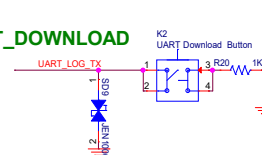
CHIP_EN



POWER LED

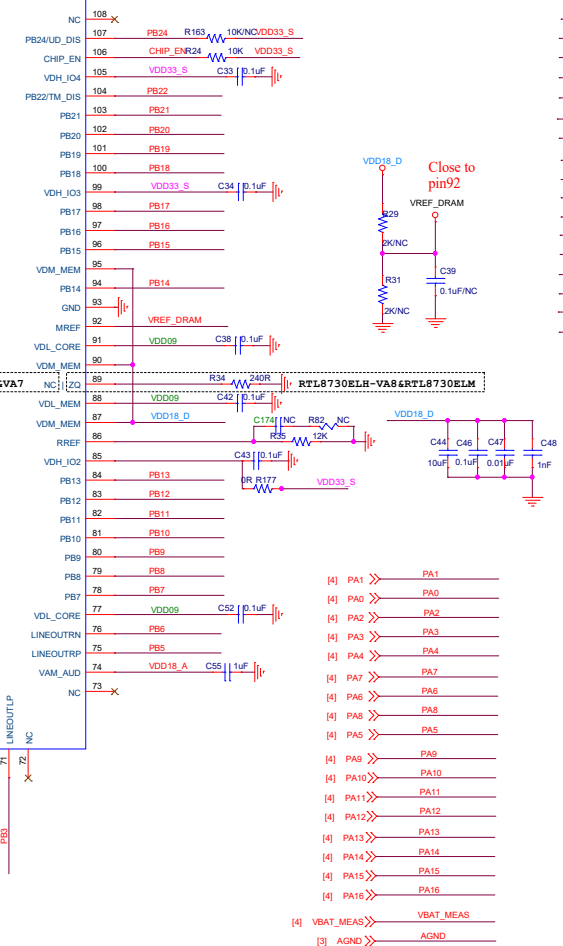
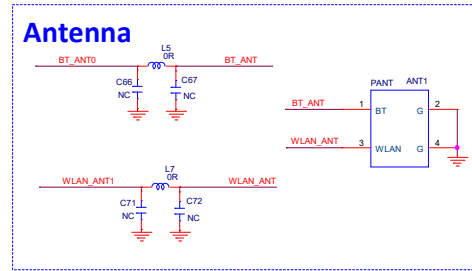
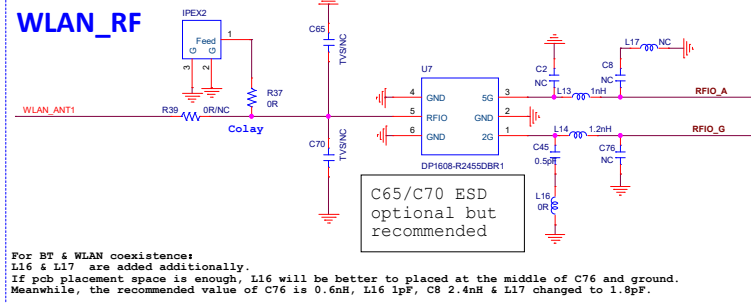
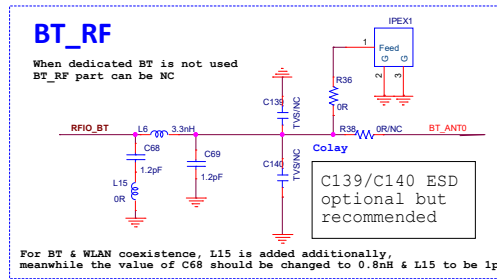
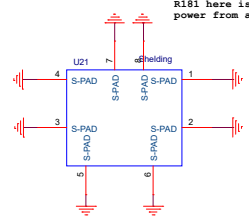
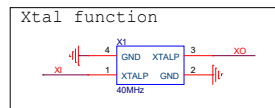
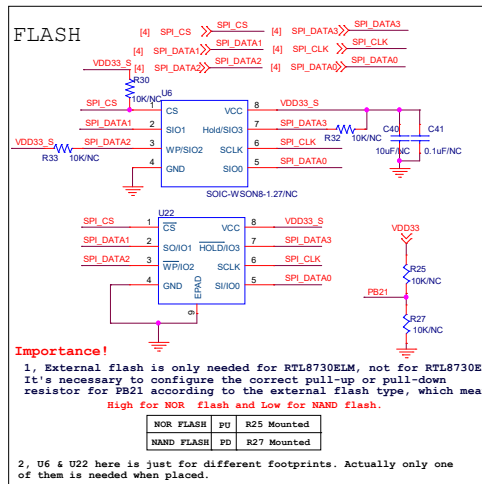
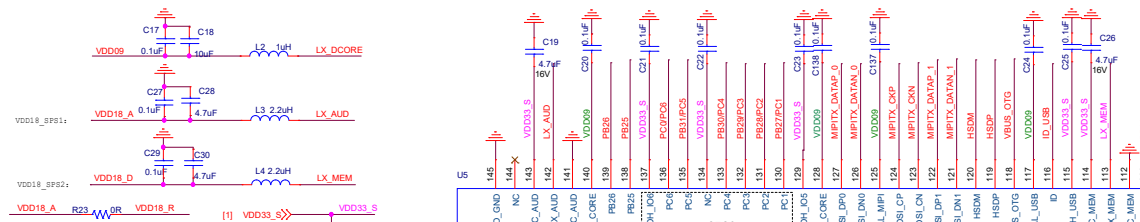


UART_DOWNLOAD

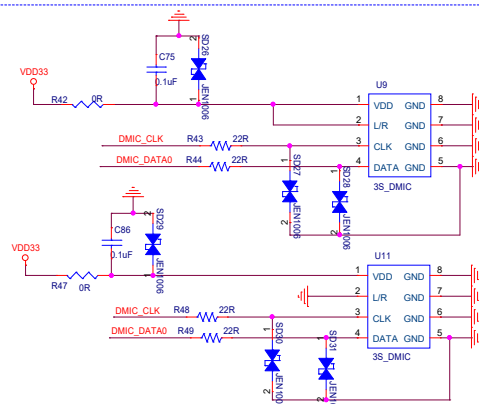


Title		EV730E1_0_USB&POWER
Size	Document Number	Ver1.0
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Date:	Friday, July 25, 2025	Sheet 1 of 4

RTL8730EL_QFN144

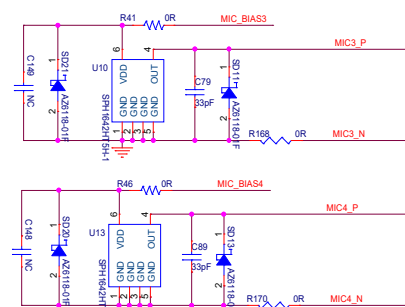
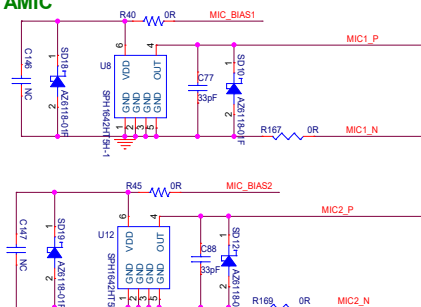


DMIC



Left : DMIC1 L/R=HIGH, CLK=HIGH, DATA=VALID
Right: DMIC2 L/R=LOW, CLK=LOW, DATA=VALID

AMIC



[1.4] VDD33 >> VDD33
[1.3.4] VDD5 >> VDD5

[2.4] PA20 >> PA20 C73 | 470nF MIC1_P
[2.4] PA21 >> PA21 C74 | 470nF MIC1_N
[2.4] PA22 >> PA22 C78 | 470nF MIC2_P
[2.4] PA23 >> PA23 C80 | 470nF MIC2_N
[2.4] PA24 >> PA24 C81 | 470nF MIC3_P
[2.4] PA25 >> PA25 C82 | 470nF MIC3_N
[2.4] PA26 >> PA26 C83 | 470nF MIC4_P
[2.4] PA27 >> PA27 C85 | 470nF MIC4_N
[2.4] PA28 >> PA28 C87 | 470nF MIC5_P
[2.4] PA29 >> PA29 C90 | 470nF MIC5_N

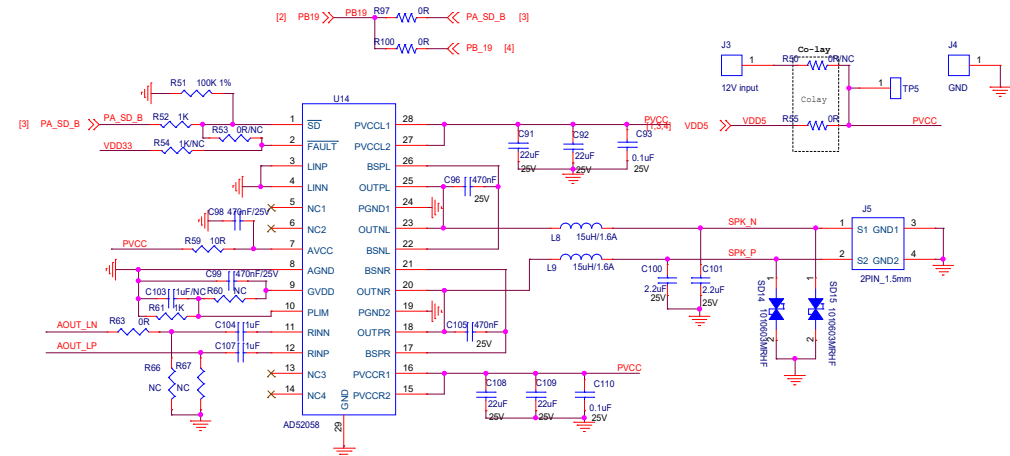
Note: MIC cap above should close to 8730E

[2.4] PA30 >> MIC_BIAS1
[2.4] PA31 >> MIC_BIAS2
[2.4] PB0 >> MIC_BIAS3
[2.3.4] PB1 >> MIC_BIAS4
[2.3.4] PB1 >> MIC_BIAS4

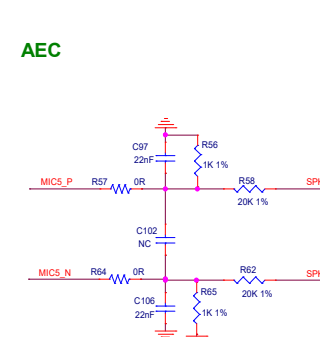
[2.4] PB4 >> AOUT_LN
[2.4] PB5 >> AOUT_RP
[2.4] PB6 >> AOUT_RN
[2.4] PB22 >> DMIC_CLK
[2.4] PB21 >> DMIC_DATA0

[2.4] PB3 >> AOUT_LP 1
[2.4] PB3 >> AOUT_LPE 2
[2.4] PB3 >> AOUT_LPE 3

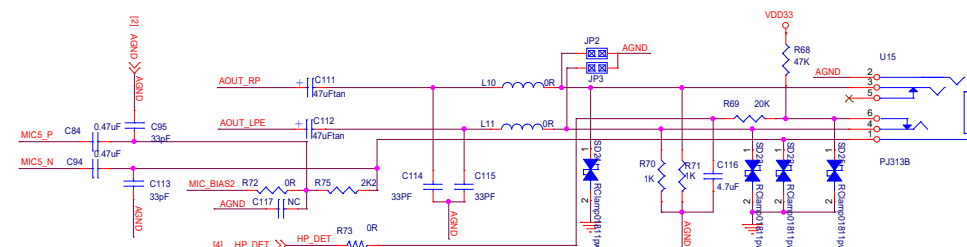
SPEAKER



AEC

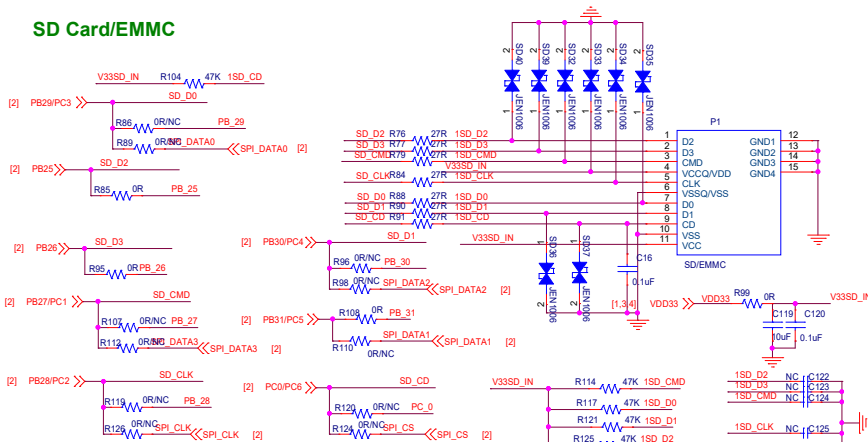


Earphone Jack (CTIA)



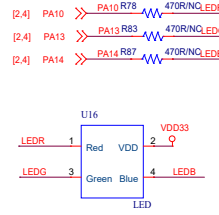
Peripheral

SD Card/EMMC

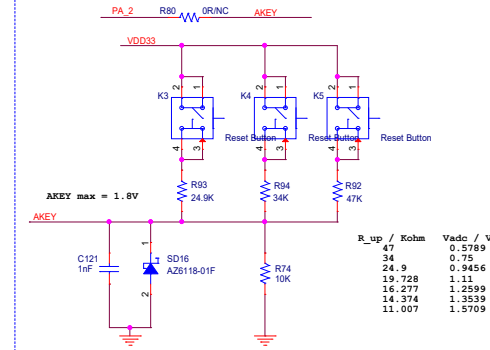


When RTL8730ELM is mounted, 0 ohm jumping resistors of GPIO nets connected with flash such as PB27, PB28, PB29, PB30, PB31, PC0 can be removed. It means these GPIOs can be directly connected to corresponding Flash nets

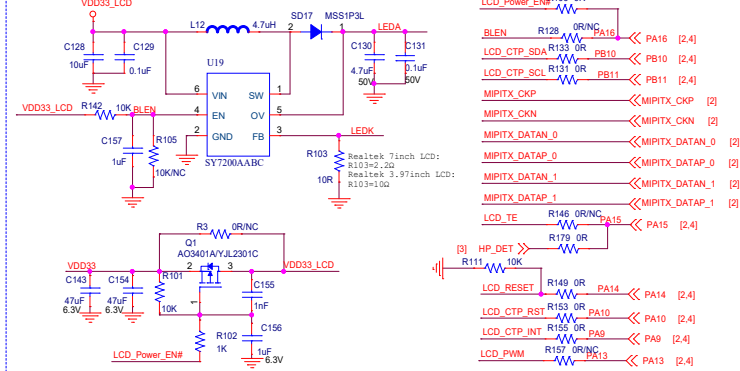
USER LED



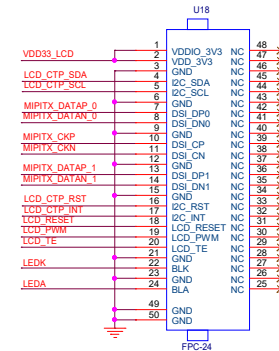
USER KEY



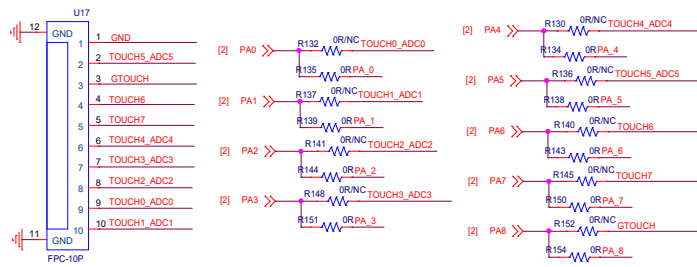
MIPI DSI



4 inch TFT LCD



CAP TOUCH



GPIOs

