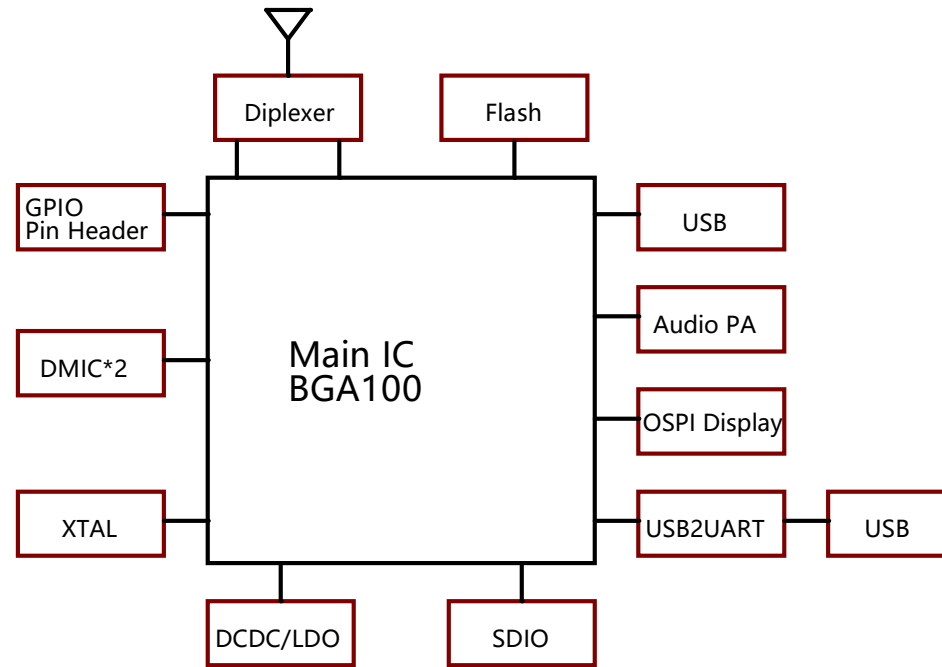
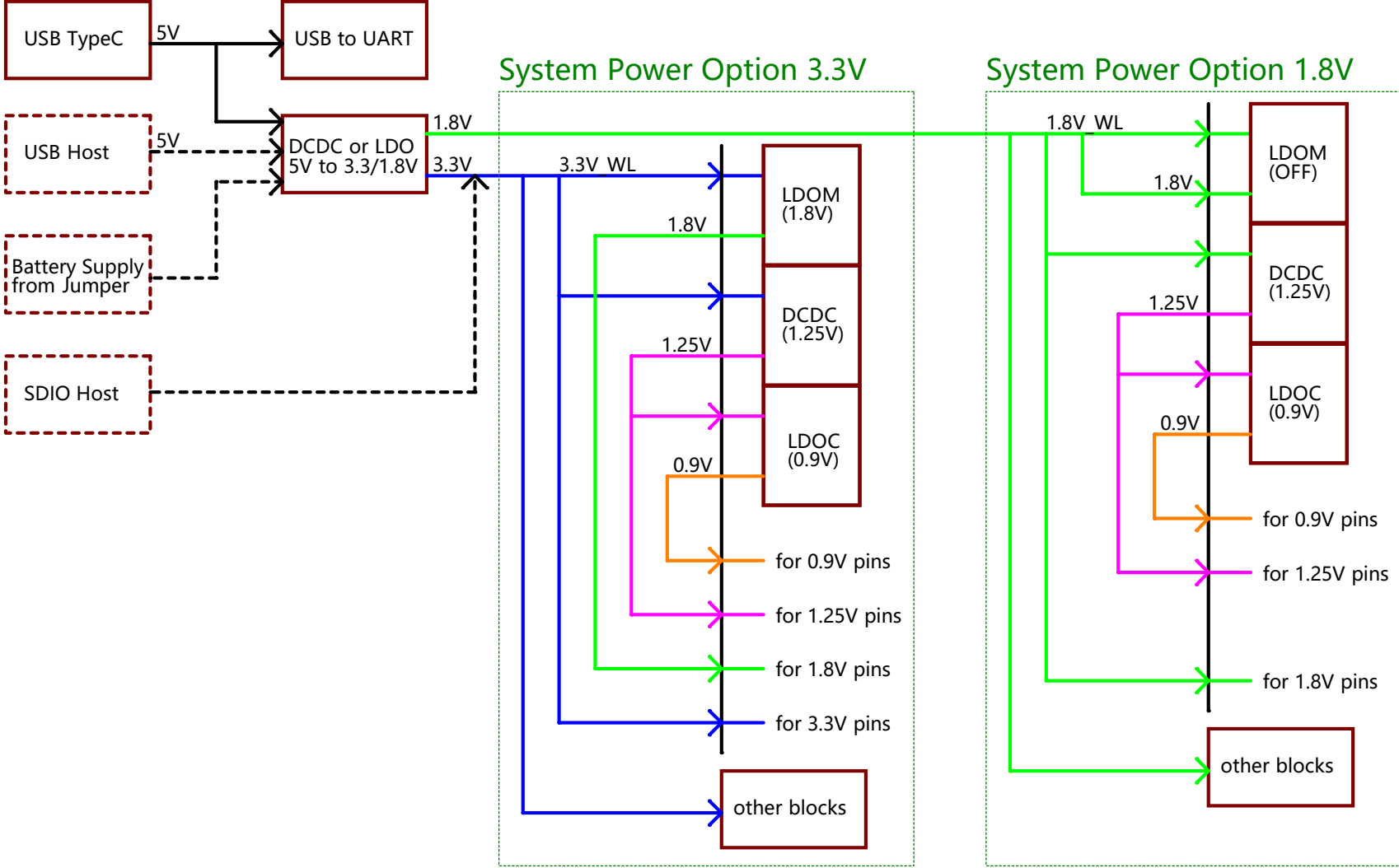


## Block Diagram

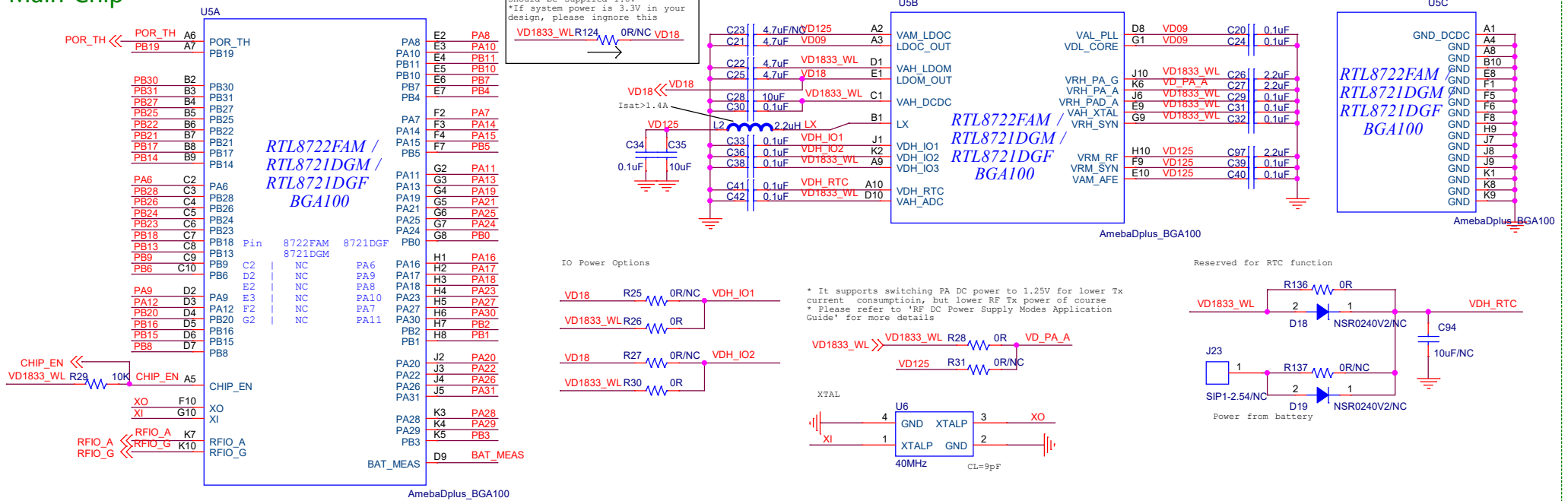


# Power Tree

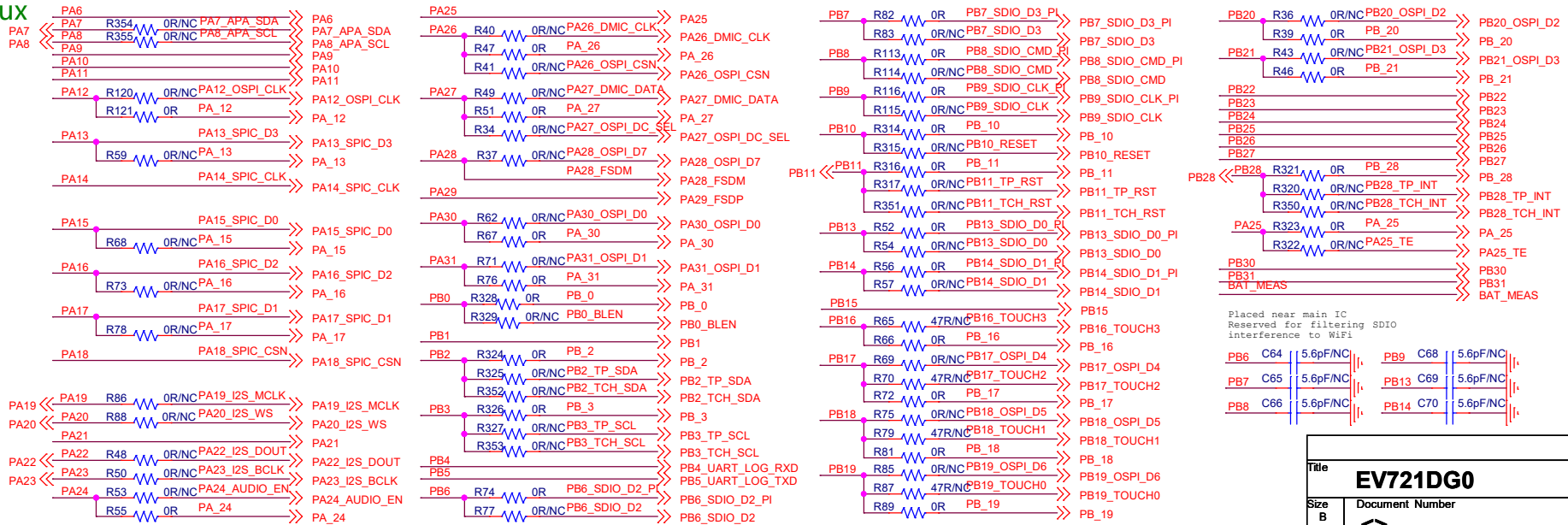





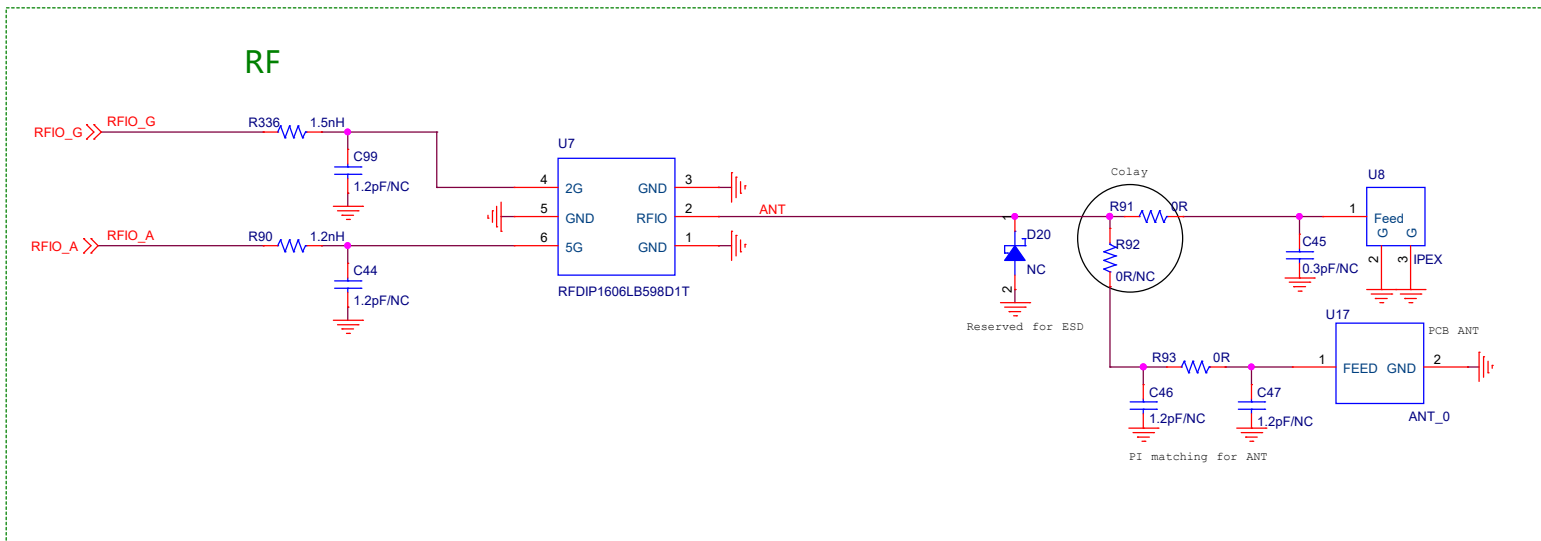
## Main Chip



## Pin Mux



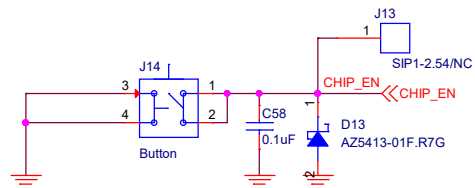
Title			
EV721DG0			
Size B	Document Number		Rev 1.0
			
Date:	Sheet	4	of 8



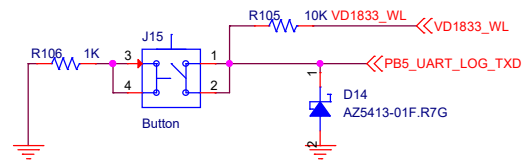
Title		
<b>EV721DG0</b>		
Size	Document Number	Rev
B	<>	1.0
Date:	Sheet 5 of 8	



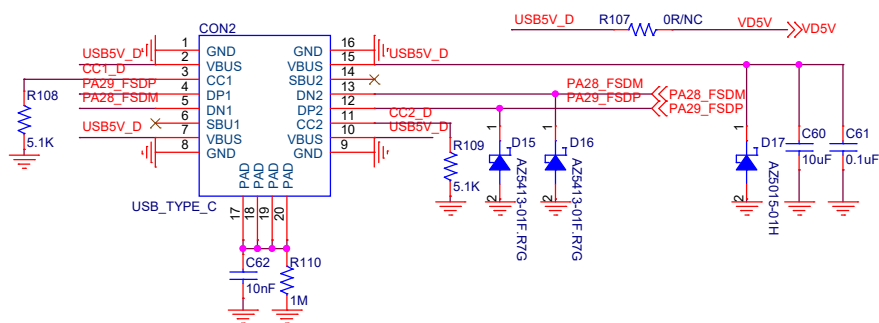
## Chip\_EN Button



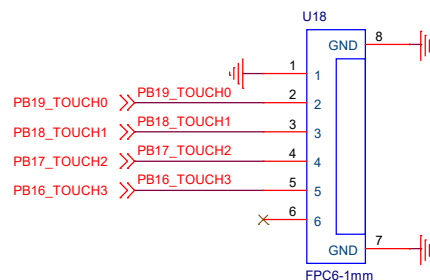
## Download Button



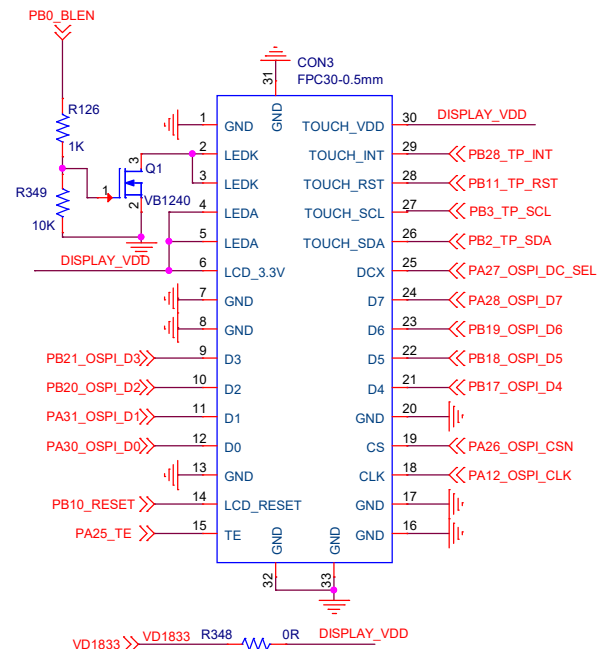
## USB Interface



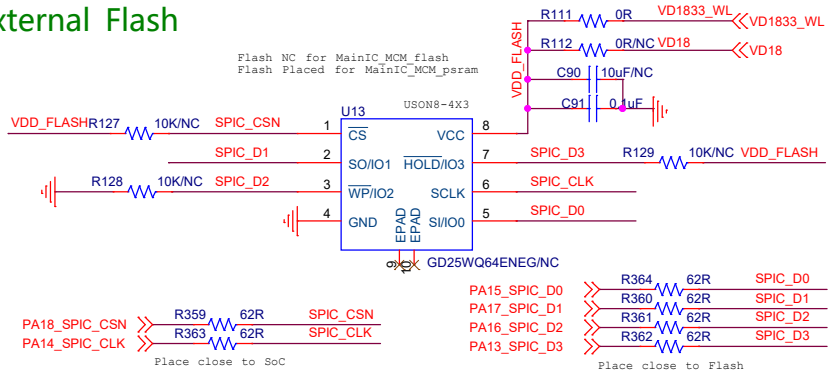
## CAP Touch Connector



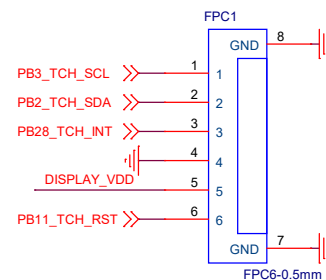
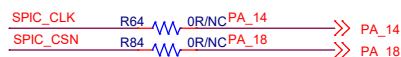
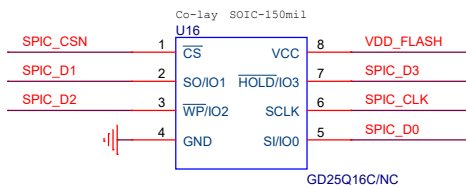
## OSPI Display Connector



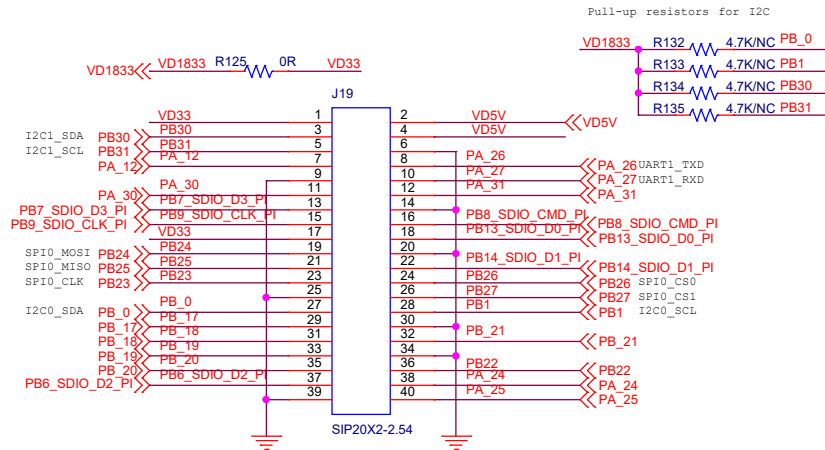
## External Flash



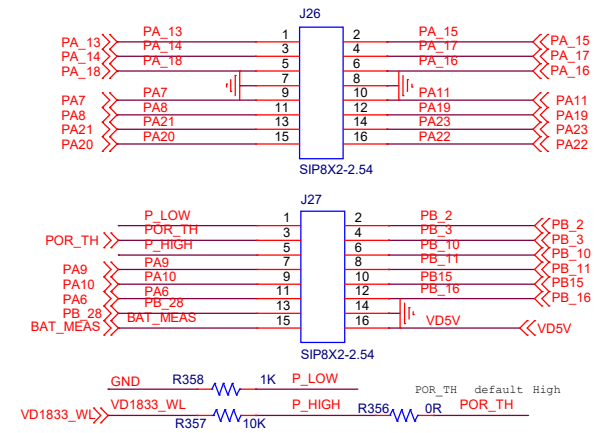
External Flash Power Option,  
1) 3.3V or 1.8V from external regulator  
2) 1.8V from internal (LDO) power



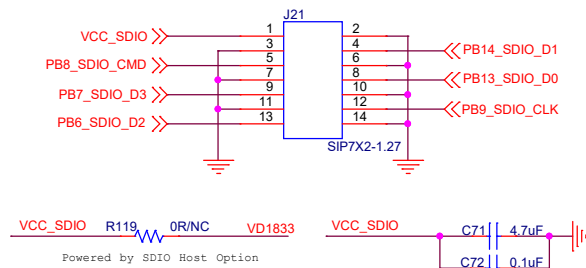
## 40 PIN Header



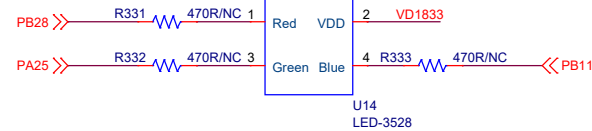
## 16 PIN Header \*2



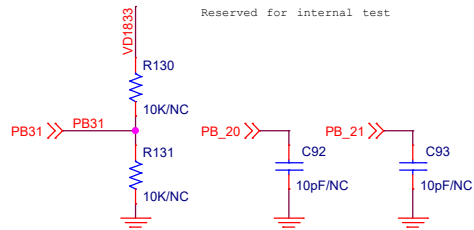
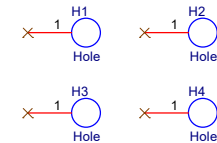
## Pin Header for SDIO Golden Finger



## User LED



## Hole



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