(Pin39 SW_LX CAP GND)
should be connected to
(Pin38 GND_SPS)

(The Capacitance of VD11_CORE
(pin25/47) & VD1833_SPS (pin40)
should be 2.2uF)
(The Capacitance of PSRAM
(pin36) should be 4.7uF)

8720DM
QFN48

QFN48-0.4-6X6

Xtal function

40MHz
XTAL-3225

FLASH

FLASH1
SOIC-8P

66Pin
Module Platform

MODULE66-27X37

RF

8721D_PANT2

C26/C27 ESD
optional but
recommended

RESET IC

SGM809-RXN3L/TR

For 3.3V application, if the power up slew rate from 1.5V to 3.0V is slower than 15ms, The wide range vcc Flash is needed or add reset IC.

In the process of power off to power on, if the power off voltage can't be guaranteed to drop below 0.3v and power on again, reset IC should be added

<Variant Name>

Title	RTL8720DM-QFN48_4V1		
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